(c) the investment involved in such projects?

THE MINISTER OF PLANNING AND THE MINISTER OF PROGRAMME IMPLEMENTATION (SHRI MADHAVSINH SOLANKI): (a) to (c) Yes Sir. According to the information available in the Ministry of Programme Implementation some of the major projects, each costing over Rs. 100 crores in the power, fertilisers, coal and steel have suffered delay due to various factors, including delays in land acquisition and in environmental clearances/equipment supply. The total investment anticipated in these projects is of the order of Rs. 25,000 crores.

2. Various steps taken by Government to expedite completion of the projects *inter alia* include—

Setting up of Task Force/Empowered Committees for problem solving and speedy implementation of poroiects.

Close follow up by concerned Ministries and project authorities with State Governments, equipment suppliers, contractors, consultants and other concerned agencies *to* minimise delays.

Inter-ministerial coordination and interaction.

Issue of detailed guidelines to facilitate the environmental clearance.

Setting ap of Integrated Circuits Design Centres

4300. SHRI K.V. THANGKABALU: Will the PRIME MINISTER be pleased to state:

(a) whether it ls a fact that Government have taken a decision to set up ten Integrated Circuits Design Centres in the country; and

(b) if so, the details and locations thereof?

THE MINISTER OF STATE IN THE MINISTRY OF SCIENCE AND TECHNOLOGY AND THE MINISTER OF STATE IN THE DEPARTMENTS OF OCEAN DEVELOPMENT, ATOMIC ENERGY, ELECTRONICS AND SPACE (SHRI K.R. NARAYANAN) :

(a) Yes, Sir.

(b) With a view to upgrading the technology of the indigenous electronics equipment, Department of Electronics, Government of India has decided to set up 10 LSI/ VLSI Design Centres in different parts of the country. The locations— which have been decided *on* the basis of the concentration and distribution of electronic equipment industry in the country areas as follows

Bangalore (the existing DOE Design Centre would be augmented), Hyderabad, Madras, Trivandrum, Pune, Baroda, NOIDA (Delhi), Lucknow, Calcutta and Bhubaneswar.

These centres will enable the design of Application Specific Integrated Circuits (ASICs) to cater to the needs of the electronics equipment industry. The ASICs design by these centres would be fabricated at Semiconductor Complex Limited (SCL)/Bharat Electronics Limited (BEL)/Indian Telephone Industries (ITI) and, if necessary, at foreign foundaries. These centres are ex. pected to be set up by November, 1988.